PRODUCT BRIEF



Digital PowerSoC Modules

From Intel[®] Enpirion[®] Power Solutions



FLEXIBLE INTELLIGENT POWER DENSE FPGA-OPTIMIZED

Footprint-Compatible Family of High-Power Digital PowerSoCs

The EM2xxx series is a family of digital 20 A, 30 A, 40 A, 60 A, and 80 A highly integrated PowerSoCs from the Intel[®] Enpirion[®] Power Solutions line. This footprint-compatible and scalable family of digital PowerSoCs is designed to simplify the complex task of building high-performance FPGA and other embedded systems.

With a highly efficient and dense footprint, a digital control architecture, and a PMBus* digital communication and control interface, the series enables meaningful system power savings and shrinks board size so that users can leverage the full capability of advanced FPGAs, ASICs, CPUs, and other embedded devices while minimizing both system power and total board space.

	EM2120	EM2130 EM2030 [†]	EM2140 EM2040 [†]	EM2260	EM2280
Load Current	20 A continuous	30 A continuous	40 A continuous	60 A continuous	80 A continuous
V _{IN} Range	4.5 V to 16 V	4.5 V to 16 V	4.5 V to 16 V	4.5 V to 16 V	4.5 V to 16 V
V _{out} Range	0.7 V to 5 V	0.7 V to 3.6 V	0.5 V to 1.325 V	0.5 V to 1.3 V	0.5 V to 1.3 V
Package Height	6.8 mm	6.8 mm	6.8 mm	5 mm	6.8 mm
Package Footprint	11 mm x 17 mm Footprint-Compatible			18 mm x 23 mm Footprint-Compatible	
Programmability and Telemetry	 PMBus* compliant interface Fully programmable and scalable through PMBus or resistor set 				

 \bullet Integrated telemetry (V_{IN}, V_{OUT}, I_{OUT}, Temperature) and system health monitoring

[†] The EM2030 and EM2040 devices offer a simple resistor programmable variant of the EM2130 and EM2140 devices where PMBus support is not required.

OPTIMIZED FOR POWER PERFORMANCE

- <0.5% accuracy and <1% ripple[†]
- Multi-mode nonlinear control loop enables fast transient response with minimal output deviation and minimal output capacitance[†]
- Defined, designed, and validated as a complete solution with Intel FPGA systems

Intel[®] FPGA V_{cc} Supply Transient Power Performance: ≤2% V_{OUT} Deviation



V_{IN} = 12 V, V_{OUT} = 0.9 V, 15 A Load Step

OPTIMIZED FOR EASE OF DESIGN

- · Highly integrated form factor and pre-configured to deliver the benefits of digital power without the complexity of digital power design
- Program, test, and customize Intel Enpirion digital PowerSoCs with Intel Enpirion Digital Power Configurator graphical user interface tool
- Family of footprint-compatible devices enables maximum design scalability, reuse, and flexibility



OPTIMIZED FOR SYSTEM DESIGN

- · Reduce bulk capacitance for a more compact and cost-effective design
- High efficiency and excellent thermal design with minimal derating eliminates the need to oversize the power supply
- Tiny total solution footprint maximizes power density and enables easier PCB design and more placement flexibility[†]
- · Implement intelligent system power management with access to real-time telemetry and comprehensive system health monitoring

EM2130 Efficiency, V_{IN} = 12 V





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¹Tests measure performance of components on a particular test, in specific systems. Differences in hardware, software, or configuration will affect actual performance. Consult other sources of information to evaluate performance as you consider your purchase. For more complete information about performance and benchmark results, visit www.intel.com/benchmarks. . Gen-1045-1.1